



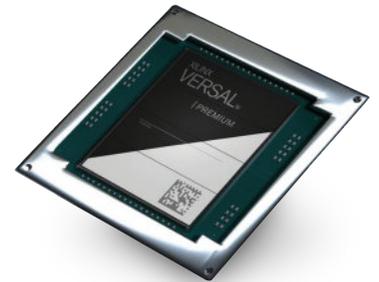
PRODUCT CONCEPT
Preliminary Product Info

Versal™ ACAP Accelerator

Ready for the most demanding compute-intensive workloads



BittWare's AX-440p is a PCIe Gen5 accelerator card designed to deliver extreme performance for data center and edge compute workloads. Featuring AMD Xilinx's 7nm Versal™ Premium ACAP device, the AX-440p is a deployment-ready NIC-sized PCIe accelerator compatible with high-performance servers. The AX-440p features a QSFP-DD for up to 400G, dual PCIe Gen5 x8, and a sophisticated Board Management Controller (BMC) for advanced system monitoring, control, and security.

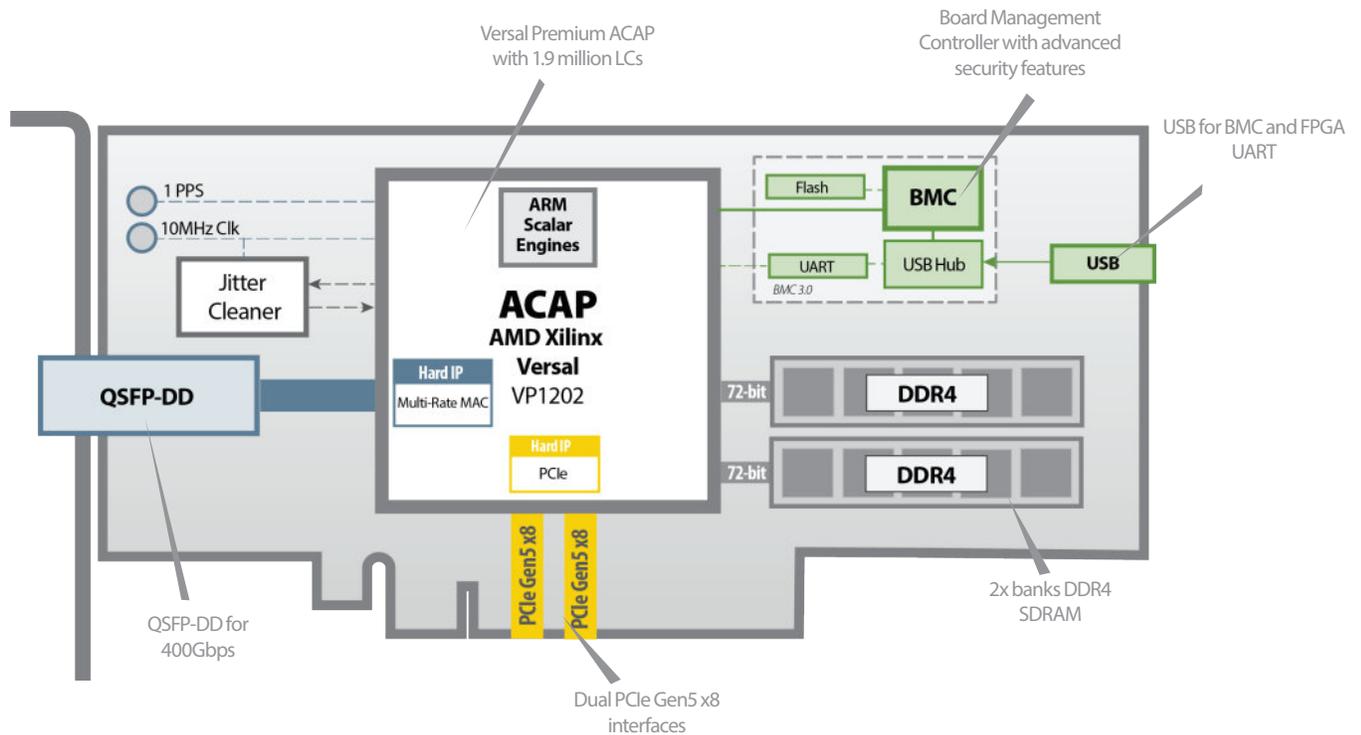


key features

QSFP-DD
for 400G

Dual PCIe
Gen5
x8

Versal ACAP
with 1.9M
Logic Cells



Additional Services

Take advantage of BittWare's range of design, integration, and support options



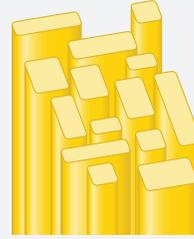
Customization

[Additional specification options](#) or [accessory boards](#) to meet your exact needs.



Server Integration

Available pre-integrated in our [TeraBox servers](#) in a range of configurations.



IP and Solutions

Our portfolio of IP and solutions reduce risk for development and deployment.



Service and Support

BittWare Developer Site provides online documentation and issue tracking.

Board Specifications

ACAP	<ul style="list-style-type: none"> Versal Premium <ul style="list-style-type: none"> VP1202 Core speed grade - 2 Contact BittWare for other ACAP options
On-board Flash	<ul style="list-style-type: none"> Flash memory for booting ACAP
External memory	<ul style="list-style-type: none"> 2x banks on-board DDR4, up to 16GB each
Host interface	<ul style="list-style-type: none"> Dual x8 Gen5 interfaces direct to ACAP, connected to PCIe Hard IP
QSFP-DD cage	<ul style="list-style-type: none"> 1x 400GbE, 2x 200GbE, 4x 100GbE, 8x 50GbE Multi-rate hard MAC Jitter cleaner for network recovered clocking
External clocking	<ul style="list-style-type: none"> 1 PPS and 10MHz ref clk inputs (in-board)
USB	<ul style="list-style-type: none"> USB access to BMC, USB-UART

Board Management Controller	<ul style="list-style-type: none"> Power sequencing and reset Voltage, current, temperature monitoring <ul style="list-style-type: none"> Protection shut-down Clock configuration Low bandwidth BMC-FPGA comms with SPI link USB 2.0 PLDM support Card-level security <ul style="list-style-type: none"> BMC Root of Trust BMC and FPGA secure boot BMC and FPGA secure upgrade Key management
Cooling	<ul style="list-style-type: none"> Standard: single-width passive heatsink
Electrical	<ul style="list-style-type: none"> On-board power derived from PCIe slot Power dissipation is application dependent Max power consumption 75W
Environmental	<ul style="list-style-type: none"> Operating temperature: 5°C to 35°C
Quality	<ul style="list-style-type: none"> Manufactured to IPC-A-610 Class 2 RoHS compliant CE, FCC and ICES approvals
Form factor	<ul style="list-style-type: none"> Low profile (half-height, half-length) PCIe slot card Optional: full-height Size: 167.7 mm x 68.9 mm

Development Tools

System development	BittWare SDK including PCIe driver, libraries, and board monitoring utilities
Application development	Supported design flows -Vivado Design Suite (HDL, Verilog, VHDL, etc.)

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BittWare
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