385R FPGA Accelerator Card

New Standard in Price Peformance

» 8X Peak TFLOPS

Altera Arria 10 FPGAs include thousands of IEEE754-compliant floating-point DSP cores that deliver up to 1.5 TeraFLOPs, and provide best performance per computing Watt

» 1.8X Bigger FPGA

More FPGA fabric provides more room for additional accelerator functions

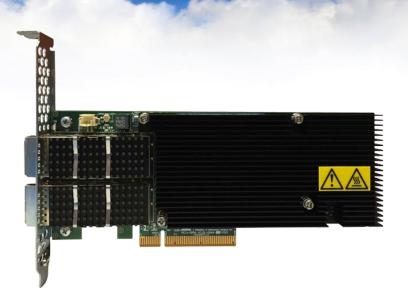
» 4X Network Bandwidth

» NIC Form Factor

- » (2) QSFP Ports configurable as:
- (2) 40GbE QSFP ports (native)
- (2) 1/10 GbE SFP ports (with adapter)
 - (8) 1/10 GbE ports (with cable)

» 33% More Memory Bandwidth

8 GBytes DDR3 memory @ 2133 MT/s



Our Latest Generation FPGA Accelerator Offers Drastically Improved Performance



OpenCL Programmable



Nallatech leads the industry with OpenCL[™] programmable FPGA solutions, opening the performance of FPGA computing to Software Engineers

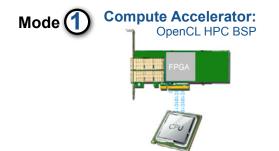
OpenCL is the first open-source, unified C-based programming model for accelerating algorithms on heterogeneous CPU-GPU FPGA platforms



The Altera SDK for OpenCL allows the easy implementation of applications onto FPGAs, providing a much faster and higher level software development flow Learn more, visit: www.Altera.com/OpenCL



Several Nallatech FPGA Accelerators are available with OpenCL Board Support Packages (BSPs), optimized for Compute and Network acceleration Learn more, visit: www.Nallatech.com/OpenCL

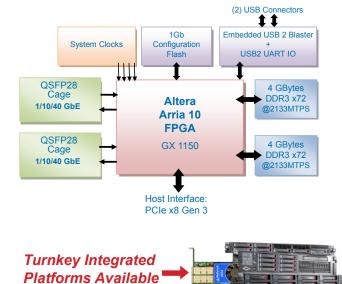


Network Accelerator: Mode **OpenCL MAC BSP**

Model Information

Product	385A
Part Number	P385A-115-11A-10
Configuration	 Arria 10 1150 GX FPGA Dual 40G QSFP28 Ports NIC Form Factor 2 Banks 4G DDR3 SDRAM @ 2133 MTPS PCIe x8 Gen 3 Active Cooling / Passive Cooling Available Upon Request 1/2 Height, 1/2 Length, Single Width PCIe Card (NIC Size)
Software & Options	 Altera Quartus Software Development Kit Edition w/ OpenCL SDK HPC BSP (Co-Processor) Included with Card Purchase MAC BSP (Network Enabled) QSFP to SFP Adapter

Block Diagram



International Distributors



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