



EDT Combo 2 Mezzanine Board – 16x E1/T1, 4x E3/T3, 16x LVDS (TIA644 standard) or RS422

Specifications

PCI / PCIe bus compliance	Related to main board; see datasheet / specifications for the main board you are using.
UI FPGA (main board)	PCI SS: Xilinx XCV1000E or optional XCV600E, XCV2000E PCI GS: Xilinx XC2VP50 or optional XC2VP70 User definable LED Debug connector, 20-pin .100" x .100" square pins, two rows
Memory (main board)	
Local	PCI SS: Two 256K x 36-bit word synchronous static RAM or optional no RAM, 512K x 36-bit word RAM, or 1M x 36-bit word RAM PCI GS: 1M x 36-bit word synchronous static RAM or optional 2M x 36-bit word RAM (with XC2VP70)
Dynamic	PCI GS: 200-pin SODIMM socket for optional DDR memory module. Up to 1 gigabyte.
LVDS / RS422	<ul style="list-style-type: none"> • Sixteen differential LVDS or RS422 signals – input or output in groups of four • Standard ECL signal levels, terminated through 50 ohms to -2V

E3 / T3	<ul style="list-style-type: none"> • Transformer coupled • Four independent E3 / T3 interfaces • Jumpers select input / output or both on eight differential connector pins • 75-ohm coaxial cable over 1100 feet at speeds up to 51.84 MB per second • Compliant with ANSI T1.102-1993, Telcordia GR-499-CORE and G.823 for jitter tolerance • B3ZS or HDB3 encoder / decoder
E1 / T1	<ul style="list-style-type: none"> • Sixteen independent input / output E1 / T1 interfaces • Jumpers select input / output or both on sixteen differential connector pins • 1.544 MB per second (T1), 2.048 MB per second (E1) • Waveforms meet G.703 and T1.102 specifications • Exceeds transmit return loss specification ETSI ETS-300166 • Jitter attenuation • Transmit return loss exceeds ETSI ETS 300166 • HDB3 or B8Zs encoder/decoder • Analog / digital and remote loopback
External connectors	High-density 68-pin AMP™ connector (part number 787169-7) High-density 15-pin AMP™ receptacle (part number 748390-5)
Physical	Dimensions: 4.2" x 6.6"
Environmental	
Temperature	Operating: 0 to 44 °C Non-operating: -40 to 70 °C
Humidity	Operating: 1% to 90% non-condensing at 40 °C Non-operating: 95% non-condensing at 45 °C
Power	2 amps at 5V

Software

Device drivers for Windows™ and Linux are included with the purchase of the board. Contact us about other operating systems.

Mezzanine board options

Combo 2 with...
Signal option: LVDS or RS422

Main board options

PCI SS main board with...
FPGA (Xilinx): XCV1000E / XCV600E / XCV2000E
Static memory (RAM): None / 512K x 36-bit word / 1M x 36-bit word

or

PCI GS main board with...
FPGA (Xilinx) + static memory (RAM): XC2VP50 with 1M x 36-bit word / XC2VP70 with 2M x 36-bit word
Dynamic memory (RAM): None / 1 gigabyte DDR

or

PCIe8 LX / FX / SX main board – for options, contact EDT

Contact



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