



## **EDT Combo 2 Mezzanine Board – 16x E1/T1, 4x E3/T3, 16x LVDS (TIA644 standard) or RS422**

### **Specifications**

<b>PCI / PCIe bus compliance</b>	Related to main board; see datasheet / specifications for the main board you are using.
<b>UI FPGA (main board)</b>	PCI SS: Xilinx XCV1000E or optional XCV600E, XCV2000E PCI GS: Xilinx XC2VP50 or optional XC2VP70 User definable LED Debug connector, 20-pin .100" x .100" square pins, two rows
<b>Memory (main board)</b>	
Local	PCI SS: Two 256K x 36-bit word synchronous static RAM or optional no RAM, 512K x 36-bit word RAM, or 1M x 36-bit word RAM PCI GS: 1M x 36-bit word synchronous static RAM or optional 2M x 36-bit word RAM (with XC2VP70)
Dynamic	PCI GS: 200-pin SODIMM socket for optional DDR memory module. Up to 1 gigabyte.
<b>LVDS / RS422</b>	<ul style="list-style-type: none"> <li>• Sixteen differential LVDS or RS422 signals – input or output in groups of four</li> <li>• Standard ECL signal levels, terminated through 50 ohms to -2V</li> </ul>

<b>E3 / T3</b>	<ul style="list-style-type: none"> <li>• Transformer coupled</li> <li>• Four independent E3 / T3 interfaces</li> <li>• Jumpers select input / output or both on eight differential connector pins</li> <li>• 75-ohm coaxial cable over 1100 feet at speeds up to 51.84 MB per second</li> <li>• Compliant with ANSI T1.102-1993, Telcordia GR-499-CORE and G.823 for jitter tolerance</li> <li>• B3ZS or HDB3 encoder / decoder</li> </ul>
<b>E1 / T1</b>	<ul style="list-style-type: none"> <li>• Sixteen independent input / output E1 / T1 interfaces</li> <li>• Jumpers select input / output or both on sixteen differential connector pins</li> <li>• 1.544 MB per second (T1), 2.048 MB per second (E1)</li> <li>• Waveforms meet G.703 and T1.102 specifications</li> <li>• Exceeds transmit return loss specification ETSI ETS-300166</li> <li>• Jitter attenuation</li> <li>• Transmit return loss exceeds ETSI ETS 300166</li> <li>• HDB3 or B8Zs encoder/decoder</li> <li>• Analog / digital and remote loopback</li> </ul>
<b>External connectors</b>	High-density 68-pin AMP™ connector (part number 787169-7) High-density 15-pin AMP™ receptacle (part number 748390-5)
<b>Physical</b>	Dimensions: 4.2" x 6.6"
<b>Environmental</b>	
Temperature	Operating: 0 to 44 °C Non-operating: -40 to 70 °C
Humidity	Operating: 1% to 90% non-condensing at 40 °C Non-operating: 95% non-condensing at 45 °C
<b>Power</b>	2 amps at 5V

## Software

Device drivers for Windows™ and Linux are included with the purchase of the board. Contact us about other operating systems.

## Mezzanine board options

Combo 2 with...  
Signal option: LVDS or RS422

## Main board options

PCI SS main board with...  
FPGA (Xilinx): XCV1000E / XCV600E / XCV2000E  
Static memory (RAM): None / 512K x 36-bit word / 1M x 36-bit word

or

PCI GS main board with...  
FPGA (Xilinx) + static memory (RAM): XC2VP50 with 1M x 36-bit word / XC2VP70 with 2M x 36-bit word  
Dynamic memory (RAM): None / 1 gigabyte DDR

or

PCIe8 LX / FX / SX main board – for options, contact EDT

Contact



Sky Blue Microsystems GmbH  
Geisenhausenerstr. 18  
81379 Munich, Germany  
+49 89 780 2970, info@skyblue.de  
www.skyblue.de



In Great Britain:  
Zerif Technologies Ltd.  
H5 Ash Tree Court  
Nottingham NG8 6PY, England  
+44 115 855 7883, info@zerif.co.uk  
www.zerif.co.uk