



EDT Combo Mezzanine Board – 16x E1/T1, 4x E3/T3, 16x ECL

Specifications

PCI / PCIe bus compliance	Related to main board; see datasheet / specifications for the main board you are using.
UI FPGA (main board)	PCI SS: Xilinx XCV1000E or optional XCV600E, XCV2000E PCI GS: Xilinx XC2VP50 or optional XC2VP70 User definable LED Debug connector, 20-pin .100" x .100" square pins, two rows
Memory (main board)	
Local	PCI SS: Two 256K x 36-bit word synchronous static RAM or optional no RAM, 512K x 36-bit word RAM, or 1M x 36-bit word RAM PCI GS: 1M x 36-bit word synchronous static RAM or optional 2M x 36-bit word RAM (with XC2VP70)
Dynamic	PCI GS: 200-pin SODIMM socket for optional DDR memory module. Up to 1 gigabyte.
ECL	Sixteen differential ECL signals; input or output in groups of four Standard ECL signal levels, terminated through 50 ohms to -2V

E3 / T3	<p>Transformer coupled</p> <p>Four independent E3 / T3 interfaces</p> <p>Jumpers select input / output or both on eight differential connector pins</p> <p>75-ohm coaxial cable over 1100 feet at speeds up to 51.84 MB per second</p> <p>Compliant with ANSI T1.102-1993, Telcordia GR-499-CORE and G.823 for jitter tolerance</p> <p>B3ZS or HDB3 encoder/decoder</p>
E1 / T1	<p>Sixteen independent input / output E1/T1 interfaces</p> <p>Jumpers select input / output or both on 16 differential connector pins</p> <p>1.544 MB per second (T1), 2.048 MB per second (E1)</p> <p>Waveforms meet G.703 and T1.102 specifications</p> <p>Exceeds transmit return loss specification ETSI ETS-300166</p> <p>Jitter attenuation</p> <p>Transmit return loss exceeds ETSI ETS 300166</p> <p>HDB3 or B8Zs encoder/decoder</p> <p>Analog / digital and remote loopback</p>
External connectors	<p>High-density 68-pin AMP™ connector (part number 787169-7)</p> <p>High-density 15-pin AMP™ receptacle (part number 748390-5)</p>
Physical	<p>Dimensions: 4.2" x 6.6"</p>
Environmental	
<p>Temperature</p> <p>Humidity</p>	<p>Operating: 0° to 44 °C</p> <p>Non-operating: -40 to 70 °C</p> <p>Heat output: TBD</p> <p>Operating: 1 % to 90 % non-condensing at 40 °C</p> <p>Non-operating: 95 % non-condensing at 45 °C</p>
Power	<p>2 amps at 5 V</p>

Software

Device drivers for Windows™ and Linux are included with the purchase of the board. Contact us about other operating systems.

Main board options

PCI SS main board with...

FPGA (Xilinx Virtex™-E): XCV600E / XCV2000E

Static memory (RAM): None / 512K x 36-bit word / 1M x 36-bit word

or

PCI GS main board with...

FPGA (Xilinx Virtex™-II Pro): XC2VP50 / XC2VP70

Static memory (RAM): 1M x 36-bit word (XC2VP50) / 2M x 36-bit word (XC2V70)

Dynamic memory (RAM): None / 1 gigabyte DDR

or

PCIe8 LX / FX / SX main board – for options, contact EDT

Contact



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