

PCIe loop back tester board

Overview

The PCIe loopback tester board enables developers and assembly factories to test and characterize the PCIe board interfaces. The board features full differential loopbacks on all the PCIe signals, JTAG interface. It also provides a 100MHz reference clock as per PCIe specification. The PCIe loopback card designed for signal integrity to support Gen3 speed of differential lanes.

Features

- PCIe FPGA board testing during development
- PCIe board electrical validation
- PCIe board testing at assembly facilities

Specifications

- Provides loopbacks on differential signals
- JTAG interface
- Reset button
- 100MHz reference clock



Ordering Code

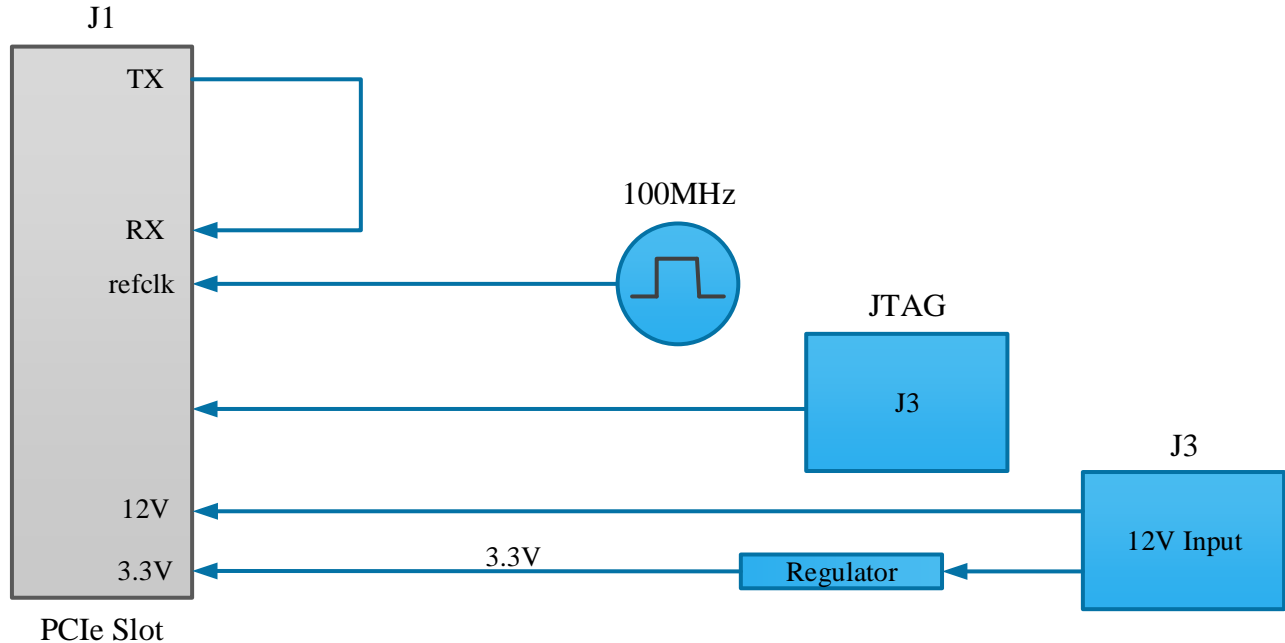
- KY-PCIE-LPBK

About PCIe

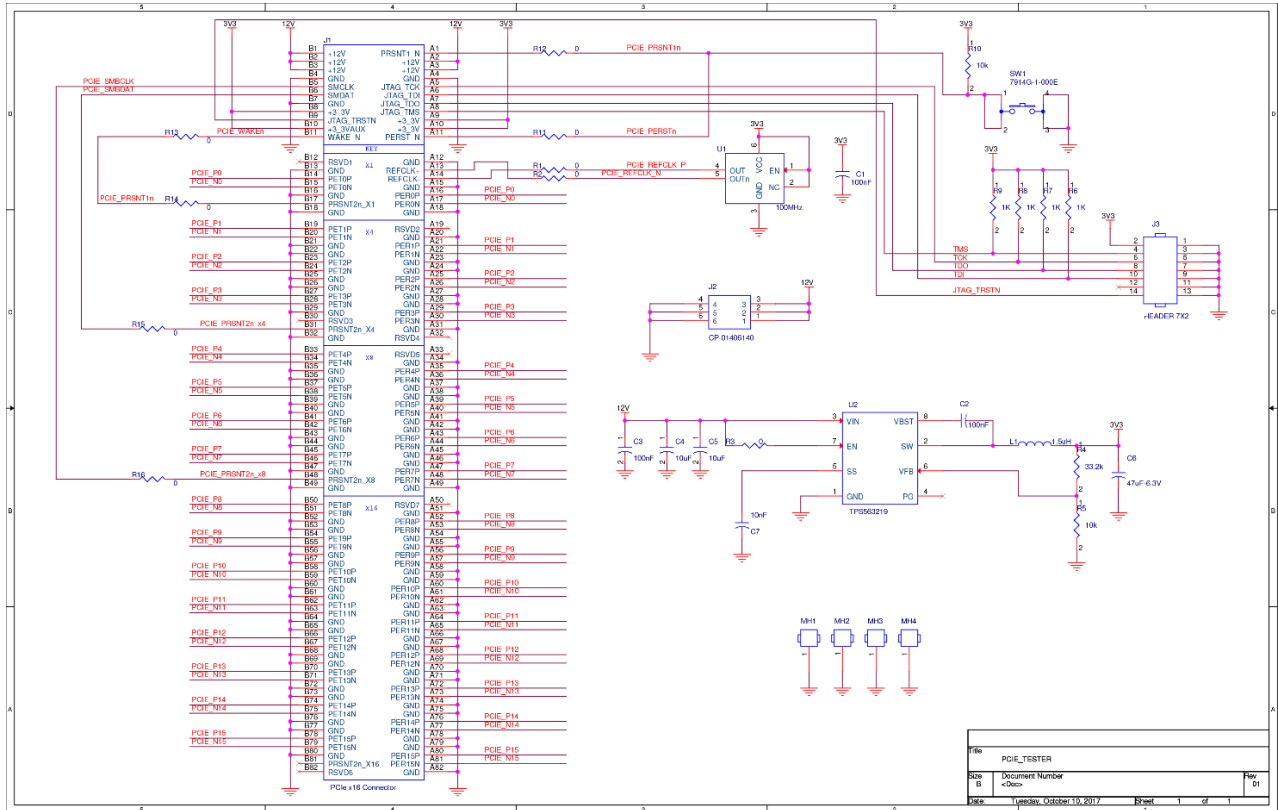
PCI Express (Peripheral Component Interconnect Express) is a high-speed serial computer expansion bus standard, including high maximum system bus throughput, low I/O pin count and small physical footprint, improved performance scaling for bus devices, detailed error detection and reporting mechanism (Advanced Error Reporting, AER), native hot-swap functionality and I/O virtualization. The primary purpose of an expansion card is to provide or expand on features not offered by the motherboard.



Block Diagram



Landscape



Contact



Sky Blue Microsystems GmbH
 Geisenhausenerstr. 18
 81379 Munich, Germany
 +49 89 780 2970, info@skyblue.de
 www.skyblue.de



In Great Britain:
 Zerif Technologies Ltd.
 H5 Ash Tree Court
 Nottingham NG8 6PY, England
 +44 115 855 7883, info@zerif.co.uk
 www.zerif.co.uk